SUBJECT CODE: 304

(c) ELECTRONICS AND COMMUNICATION ENGINEERING (DEGREE STANDARD)

UNIT - I: SEMICONDUCTOR THEORY AND ELECTRONIC DEVICES
Intrinsic and extrinsic semiconductors, Energy Band Diagrams, Diffusion and Drift current densities, Hall effect. PN junction diode, current equation, Transition and Diffusion capacitances, Zener diode, Tunnel diode, Varactor diode, Photo diode, Schottky diode, LED, BJT, FET, JFET, MOSFET, SCR, UJT, TRIAC, IC fabrication.

UNIT - II: CIRCUIT THEORY, SIGNALS AND SYSTEMS

UNIT - III: ANALOG ELECTRONIC CIRCUITS

UNIT - IV: CONTROL SYSTEMS AND INSTRUMENTATION
Control system components, feedback, transfer function, transient and steady analysis of LTI systems, Frequency response, Bode, Polar, Nyquist plots, Routh-Hurwitz and Nyquist stabilities, Lag, Lead, Lag-lead compensation, State variable model.

UNIT – V: ELECTRONIC COMMUNICATION
AM, FM, PM modulation and demodulation, Superheterodyne receiver, AGC, PAM, PWM and PPM, Entropy, Mutual information, Channel capacity, PCM, DPCM, ADPCM, DM, ADM, Source encoding techniques, TDM and FDM, line coding techniques, ASK, FSK, PSK, QPSK, QAM – Bandwidth, SNR, BER, Error Probability, Eye Diagram, Bandpass Sampling, clock and carrier synchronization, Error control coding, Spread spectrum modulation methods.

UNIT – VI: ELECTROMAGNETIC FIELDS AND ANTENNAS
Theorems: Divergence, Stokes, Coulomb; Poisson and Laplace Equation, Ampere’s law, Biot-Savort law, Gauss law for magnetic fields, Maxwell's equations, Displacement current, Uniform plane waves, Polarization, reflection and refraction of plane waves at different boundaries, Poynting vector.

Transmission line equation, Characteristic impedance, impedance matching, Smith chart, Attenuators and Equalizers, Lattice diagram, TE, TM and TEM waves, Rectangular guides,
Dielectric slab wave guides, TE and TM wave in circular guides, Cavity resonator and Q for dominant mode.

Antennas: Diploe, Horn, Reflector, Slot, spiral, logperiodic microstrip; Broadside and End fire array, adaptive array, antenna gain, radiation pattern, polarization, VSWR, Radiowave propagation.

UNIT – VII: COMMUNICATION SYSTEMS
Wireless Link budget, Wireless channel characteristics: coherence bandwidth, Doppler spread; Flat, Frequency selective, Fast and slow fading; FDMA, TDMA, CDMA, Capacity calculation, Frequency reuse, Channel assignment, Handoff, trunking and grade of service. Minimum shift keying Gaussian Minimum shift keying, OFDM, cyclic prefix, PAPR, Adaptive equalization, Diversity, Rake receiver, MIMO Systems, Beam forming, Capacity in fading and non-fading channels.


Light Propagation in optical fibres, Ray and mode theory, Fibre structure, Fibre materials, merits of optical fibre communication, Fibre attenuation and dispersion characteristics, Materials for optical sources, LED and LASER Diodes, Optical detection, PIN and Avalanche Photo diodes, WDM Concept, optical networks.

Satellite orbits – Kepler's laws, Geostationary satellite, transponders, GPS receiver DBS/DTH.

OSI/TCP/IP model - functions and protocols of layers, Routing algorithms, Congestion control algorithms, MAC Protocols.

UNIT – VIII: DIGITAL SIGNAL AND IMAGE PROCESSING
DFT, FFT, Overlap and save methods, Butterworth and chebyshev filters, impulse invariant and bilinear transform methods, realization structures, FIR design methods, product quantization, limit cycle oscillations, scaling, Decimation and interpolation, multirate signal processing.

Brightness, Contrast, Hue, Saturation, RGB, HSI Models, Mach band Effect, Image sampling, DCT, Histogram Equilization, Mean and median filters, Region growing segmentation, JPEG standard.

UNIT – IX: VLSI AND EMBEDDED SYSTEMS
UNIT – X: COMPUTER ENGINEERING
Number systems, Boolean algebra, Karnaugh map, logic gates, Adders, magnitude comparator, Decoder, Encoder, Mux, Demux, Fliplops, Counters, shift register, Synchronous sequential circuits, Asynchronous sequential circuits, ROM, EPROM, EEPROM.

Fundamentals of Computer architecture, Data path and control unit design, RAM, Optical, Cache and Virtual Memories, Memory allocation, Associative memory, DMA, interrupts, RISC and CISC processors.