

DEPARTMENT OF ELECTRICAL ENGINEERING D. No. 3617 / इन्दौर / E.E.D.
 ZAKIR HUSAIN COLLEGE OF ENGINEERING AND TECHNOLOGY दिनांक/Date 19/3/2020
 ALIGARH MUSLIM UNIVERSITY, ALIGARH अध्यक्ष कार्यालय, इन्दौर
 Chairman's Office, E.E.D.

Advertisement No: 1/ CRGS/Mohd Tariq/01/March 2020

Dated: 19 March 2020 (Thursday)

Applications on email along with attested copies of supporting documents with complete bio-data (including contact number and email id) are invited for the positions of **Senior Research Fellow (01)** and **Junior Research Fellow (01)** in the Australian Collaborative Research Grant Scheme (CRGS) project. The selection process will be as per CRGS, Australia rules. The details of the project are as follows:

Name of the Project	Design and Development of Novel Control Algorithms for Improving Power Quality Issues in Power Converters.
Principle Investigator	Dr. Mohd Tariq Department of Electrical Engineering Aligarh Muslim University, Aligarh, UP, India Email: tariq.ee@zhcet.ac.in
Eligible Branches of Engineering:	Electrical Engineering
Essential/ Desirable Qualifications	- M. Tech. in relevant Branch - Publications in SCI indexed Journals
Emoluments (Consolidated)	Rs. 32,000 pm for SRF and Rs 28,000 for JRF PM
Nature of post	The position is purely temporary and is initially for six (06) months.
Prospect	Applicants wanting to continue in Ph.D. will be preferred for JRF
Application procedure	Interested candidates are requested to send the application form with self-attested copies of educational qualifications, experience certificate(s), GATE qualification certificate, copies of publications (in SCI indexed journals only), awards, recommendations, etc, and must reach the email (tariq.ee@zhcet.ac.in) and a hard copy in the office of the chairman, electrical engineering department latest by 12:00 Noon on 03- April- 2020 (Friday) .
Application form	The Application form is attached with this advertisement. It can also be downloaded from the webpage of Dr. Mohd Tariq.

Note:


- TA/DA will not be paid for appearing in the interview.
- The selection process is expected to be held in the second week of April 2020. The date of interview will be intimated through email to the shortlisted candidates. The applicants are required to check their emails on regular basis as no separate letter will be issued.


 Dr. Mohd. Tariq

Principle Investigator and Assistant Professor
 Department of Electrical Engineering
 Aligarh Muslim University, AMU, Aligarh

Copy to:

1. Dean, F/o Engineering & Technology, AMU Aligarh
2. Principal, Z.H. College of Engineering & Technology, AMU, Aligarh
- ✓ 3. Director, Computer Centre with the request to upload the same on the University website
4. Registrar, AMU Aligarh for kind information
5. Deputy Finance Officer (Grant Section) AMU Aligarh
6. Notice Board: - EED and Principal, ZHCET, AMU, Aligarh
7. Neighbouring Institutions (by email)


 Dr. Mohd. Tariq
 (Principle Investigator)

DEPARTMENT OF ELECTRICAL ENGINEERING
ZAKIR HUSAIN COLLEGE OF ENGINEERING AND TECHNOLOGY
ALIGARH MUSLIM UNIVERSITY, ALIGARH

पत्रांक सं./D. No. 3632/इंजिनी/ई.ई.डी.

दिनांक/Date 21/3/2020

अध्यक्ष कार्यालय, इंडोरी

Chairman's Office, E.E.D.

ए०एमयू अलीगढ़/AMU, Aligarh

Date: 20.03.2020

Corrigendum to Advertisement (Advt. No.: 1/CRGS/Mohd Tariq/01/March 2020)

With reference to the advertisement (Advt. No.: 1/ CRGS/Mohd Tariq/01/March 2020) dated 19.03.2020, the following amendments in the qualification details are to be noted in the advertisement:

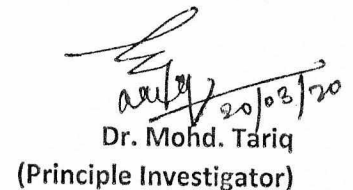
S.N.	Designation	Qualifications
1	Junior Research Fellow (JRF)	Essential: - 1. B.Tech./B.E./B.Sc. in Electrical Engineering. 2. M. Tech./M.E. in relevant Branch. 3. Qualified GATE in Electrical Engineering. Desirable: - 1. Publications in SCI indexed Journals
2	Senior Research Fellow (SRF)	Essential: - 1. B.Tech./B.E./B.Sc. in Electrical Engineering. 2. M. Tech./M.E. in relevant Branch. 3. Qualified GATE in Electrical Engineering. 4. Two years research/teaching experience. Desirable: - 1. Ph.D. in relevant research area. (Ph.D. students currently enrolled can also apply) 2. Publications in SCI indexed Journals


Dr. Mohd. Tariq

Principle Investigator and Assistant Professor
Department of Electrical Engineering
Aligarh Muslim University, AMU, Aligarh

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Dr. Mohd. Tariq
(Principle Investigator)

**DEPARTMENT OF ELECTRICAL ENGINEERING
ZAKIR HUSAIN COLLEGE OF ENGINEERING AND TECHNOLOGY
ALIGARH MUSLIM UNIVERSITY, ALIGARH**

APPLICATION FORM FOR THE POST OF SRF/JRF IN THE DEPARTMENT

INSTRUCTIONS TO THE CANDIDATE:

1. All the applications duly filled must be emailed on tariq.ee@zhcet.ac.in (soft copy) and hard copy in the office of the Chairman, Department of Electrical Engineering, AMU, Aligarh latest by **03.04.2020 (Friday)** by **12:00 noon**.
2. The last date of submission will be regarded as the last date for the postal delivery of the form in the department.
3. In case of holiday on the last date of submission, the next working day will be considered as the last date of submission.
4. No application will be entertained after the last date.
5. The candidate is requested to keep the acknowledgement received from the department safe, as no query regarding the form will be entertained without its demonstration.
6. Candidates sending the form through postal delivery will receive the acknowledgement notification on their email id latest by 04.04.2020.
7. No TA/DA will be provided to the candidate.
8. The candidates have to bring FIVE copies of this form, along with ONE set of the publications (of SCI indexed journals only) and patents, at the time of interview.
9. Candidates already in job must bring the No objection certificate from their employer on the day of interview.
10. The PI in consultation with the funding agency of the project reserves the right to call or not to call any candidate for the interview. The summoning of candidates for interview, conveys no assurance whatsoever that they will be selected.
11. The selected candidate/s will be intimated through their email. No correspondence will be made with candidates who are not selected.

Passport size recent
Photograph
duly signed by the
applicant

APPLICATION FORM

1. Post applied for (JRF/SRF/Both) :
2. Candidate's Name in full (Mr./Mrs./Miss):(In Capital Letters)
3. (a) Father's Name:(b) Mother's Name:
4. Address:

Permanent:	Correspondence (Leave blank if same as permanent):
Email:	Mobile No:

5. Date of Birth (in Christian era) :/...../..... (dd/mm/yyyy)
6. (a) Marital Status: (b) Nationality:
7. Educational Qualifications (from highest to lowest):

Sl. No.	Examination	Subject	Percentage	Univ./Board	Year of Passing

8. Title of PhD Thesis:.....
9. Title of M. Tech Dissertation/Thesis:

10. GATE Details:

Year	Roll No.	Score	Qualified (Yes/No)

11. Details of Research papers published/accepted in (SCI Indexed Journals) only:

S. No.	Title of paper	Transaction/Journal	IF	Year
1.				
2.				
3.				

Kindly attach a separate sheet if the number of papers is more than 3. Provide first page of the paper as proof.

12. Patents:

a. Granted:

b. Published:

13. Write a few lines (if any) on your experience with Real-time emulators (Typhoon HIL, PLECS, OPAL-RT, etc):

.....

.....

.....

.....

I, hereby solemnly declare that the entries made by me in the above form are true to the best of my knowledge. If any entry is found incorrect, the form may be rejected.

Signature of the candidate (in full)

Date and Place:.....

ACKNOWLEDGEMENT

This is to acknowledge that.....(Name) has submitted the form for the post of.....(SRF/JRF/Both) on against the advertisement 1/ CRGS/Mohd Tariq/01/March 2020 (Advt No.) dated 19 March 2020 (Thursday).

Signature and name of the receiving authority.

Date: