



**Indian Institute of Technology Gandhinagar
Palaj, Gandhinagar
Gujarat – 382355**

09/03/2022

Advertisement

Applications are invited from bright, young candidates for the following post under a DST funded project in Discipline of Electrical Engineering.

Name of Post : Junior Research Fellow (JRF)

Consolidated Monthly Pay : Rs. 31,000/- (consolidated)

Qualification and Experience : BE/BTech/ME/MTech in Electrical/Electronics and related disciplines with >75% marks (or > 8.0 CPI), valid GATE score and knowledge in Semiconductor Devices or Microelectronics or equivalent. The candidate should be proficient in Technology CAD tools like Synopsis Sentaurus or Silivaco. Experience in semiconductor device modeling, device characterization and LINUX operating system will be preferred. The applicant should be driven, enthusiastic about research and willing to work for the duration of the contract.

Tenure : 1 year extendable till end of project on satisfactory performance

Self-motivated students (fulfilling the above eligibility criteria), who are willing to work in the area of semiconductor devices and technology are encouraged to send me a mail at (**nihar@iitgn.ac.in**) on or before **8th April 2022**. The selection will be based on interview and shortlisted candidates will be intimated well in advance the date and time of the interview.

Queries may be directed to Dr. Nihar Ranjan Mohapatra (Professor, EE, IIT Gandhinagar).