Postdoctoral Fellow in Computational Complexity

August 8, 2022

Indian Institute of Technology Gandhinagar (IITGN) invites applications for a Research Associate (Postdoctoral Fellow) in Computational Complexity Theory for a research project funded by co-funded by SERB-SRG and Indian Institute of Technology, Gandhinagar.

Job Description The candidate will work on the project "Hazard-free Circuits: Algorithms and Complexity". This would involve a mathematical study of hazard-free circuits, which is a sub-area of Boolean circuit complexity.

Eligibility PhD in Complexity Theory, Theory of Algorithms, or related areas with a good scientific publication record. Experience working in Boolean circuit complexity is a plus. Candidates who have submitted their thesis are also eligible for the position.

Duration One year.

Remuneration 50,000 INR per month plus 10,000 INR HRA

Application Procedure Candidates must send following documents via e-mail to Dr. Balagopal Komarath (bkomarath at iitgn dot ac dot in). The following documents must be sent as a single pdf attachment to the email:

1. Current CV with a list of publications.

2. A one-page summary of previous research/work experience.

3. A one-page write-up about why you want to take up this postdoc position.

4. Names and email addresses of 2 potential referees.

The applications will continue to be screened until the position is filled. The applications received before 30th August, 2022 will be given priority. Queries may be directed to Dr. Balagopal Komarath via email.