

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE, PILANI (RAJ.)

Advt. No. PF-08/2022

Date: 19.10.2022

“Online Interview”

CSIR-Central Electronics Engineering Research Institute (CSIR-CEERI) is a premier research Institute in the field of Electronics, set up under the Council of Scientific & Industrial Research (CSIR). The Institute is looking for qualified candidates for the following project staff positions purely temporary engagements for various ongoing projects in the Institute.

Online applications are invited from candidates for Engagement of Project Staff.

Details of Vacancies & Eligibility

Post Code	Project No.	Area/Group	Name & Number of Vacancy	Qualifications	Age	Consolidated Emoluments per month (fixed)
2901	HCP- 0026	Integrated circuits & systems	Project Associate-II 01	B.E. / B.Tech./ M.Sc. (ECE/ Computer Science/ IT/ Electrical/ Electronics) or equivalent having 02 Years’ experience in Research & Development in Industrial and Academic Institutions or Science and Technology organisations and Scientific activities and Services. Desirable: M.E./ M.Tech (CS/ Embedded System/ Data Science) or equivalent having 02 Years’ of experience. Special skills required: Machine vision, Data Fusion, Data Modelling, Machine Learning, Deep Learning, Embedded System, Circuit Design, Experience with various microcontroller. Language: C, C++, Phyton, Java, CSS, HTML.	35 Years	*₹35,000/- +HRA as per rules OR *₹28,000/- +HRA as per rules
2902	HCP- 0042 (WP- 5.3)	Advanced Electronic System Area	Project Associate-II 02	B.E./ B.Tech/ M.Sc (CSE/ ECE/ EEE/ IT/ Robotics/ Mechatronics/ Instrumentation) or equivalent having 02 Years’ experience in Research & Development in Industrial and Academic Institutions or Science and Technology organisations and Scientific activities and Services.. Desirable: M.Tech/ M.E (CSE/ ECE/ EEE/ IT/ Robotics/ Mechatronics/ Instrumentation) or equivalent and working knowledge of Python coding, Machine learning and deep learning, ROS, Software development or drone navigation & control.	35 Years	*₹35,000/- +HRA as per rules OR *₹28,000/- +HRA as per rules

2903	HCP- 0034 (WP- 1.6)	High Power Microwave System Group	Project Associate-I 01	B.E./ B.Tech/ M.Sc (Electronics/ Physics/ ECE/ EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave) or equivalent. Desirable: M.E./ M.Tech (Electronics/ Instrumentation/ Instrumentation & Control Systems/ Electrical Engg./ Microwave Systems) or equivalent. Candidates having experience in design and development of plasma assisted devices and sources.	35 Years	#₹31,000/- +HRA as per rules OR #₹25,000/- +HRA as per rules
2904	HCP- 0042	Advanced Electronics Systems Area	Project Associate-II 01	B.E. / B.Tech./ M.Sc. (CSE/ IT) or equivalent having 02 Years' experience in Research & Development in Industrial and Academic Institutions or Science and Technology organisations and Scientific activities and Services.	35 Years	*₹35,000/- +HRA as per rules OR *₹28,000/- +HRA as per rules
2905	HCP- 0034 (WP- 1.1)	Vacuum Electron Devices Group	Project Associate-I 01	B.E./ B.Tech/ M.Sc (ECE/ Electronics) or equivalent.	35 Years	₹25,000/- +HRA as per rules
2906	HCP- 47	High Frequency Devices and System Group	Project Associate-I 01	B.E. / B.Tech./ M.Sc (Engineering/ Natural Sciences) or equivalent. Desirable: Knowledge of data handling and analysis.	35 Years	#₹31,000/- +HRA as per rules OR #₹25,000/- +HRA as per rules
2907	HCP-0042 (WP- 1.4)	Advanced Electronic Systems Area	Project Associate-I	B.E. / B.Tech./ M.Sc (ECE/ Computer Science/ IT/ Electrical/ Electronics) or equivalent. Desirable: working experience and domain knowledge of areas of AI algorithm development or circuit design.	35 Years	#₹31,000/- +HRA as per rules OR #₹25,000/- +HRA as per rules
2908	HCP- 0042 (WP- 2.4)	Advanced Electronic Systems Area	Project Associate-I 01	B.E./ B.Tech/ M.Sc (EEE/ Electronics and Instrumentations/ Electronics/ ECE/ CSE) or equivalent. Desirable: Working experience and domain knowledge of AI, Machine Learning Algorithms, Deep Learning Algorithms and Python.	35 Years	#₹31,000/- +HRA as per rules OR #₹25,000/- +HRA as per rules
2909	HCP- 0042 (WP- 2.4)	Advanced Electronic Systems Area	Project Associate- II 01	B.E./ B.Tech/ M.Sc (EEE/ Electronics and Instrumentations/ Electronics/ ECE/ CSE) or equivalent having Two Years' of experience in R&D in Industrial and Academic Institutions or Science and Technology, Organisations and Scientific Activities and Services in the areas of AI, Machine Learning	35 Years	*₹35,000/- +HRA as per rules OR *₹28,000/- +HRA as per rules

				Algorithms and Deep Learning Algorithms (with domain Knowledge of Python). Desirable: Working experience and domain Knowledge of AI, Machine Learning Algorithms, Deep Learning Algorithms and Python.		
2910	HCP- 0042 (WP- 4.2)	Advanced Electronic Systems Area	Project Associate- I 01	B.E./ B.Tech/ M.Sc (EEE/ Electronics and Instrumentations/ Electronics/ ECE/ CSE) or equivalent. Desirable: Working experience and domain knowledge of AI, Machine Learning Algorithms, Deep Learning Algorithms and Python.	35 Years	₹31,000/- +HRA as per rules OR ₹25,000/- +HRA as per rules
2911	HCP- 0042 (WP- 5.1)	Advanced Electronic Systems Area	Project Associate- I 01	B.E./ B.Tech/ M.Sc (EEE/ Electronics and Instrumentations/ Electronics/ ECE/ CSE) or equivalent. Desirable: Working experience and domain knowledge of AI, Machine Learning Algorithms, Deep Learning Algorithms and Python.	35 Years	₹31,000/- +HRA as per rules OR ₹25,000/- +HRA as per rules
2912	HCP- 0042 (WP- 5.2)	Advanced Electronic Systems Area	Senior Project Associate 01	B.E./ B.Tech/ M.Sc (EEE/ Electronics and Instrumentations/ Electronics/ ECE/ CSE) or equivalent having Four Years' of experience in R&D in Industrial and Academic Institutions or Science and Technology Organisations and Scientific Activities and Services in the areas of AI, Machine Learning Algorithms and Deep Learning Algorithms (with domain knowledge of Python). OR M.E./ M.Tech./ M.S (EEE/ Electronics and Instrumentations/ Advanced Electronics Engg./ Electronics/ ECE/ CSE/ Embedded Systems) or equivalent having Two years' of experience in R&D in Industrial and Academic Institutions or Science and Technology Organisations and Scientific Activities and Services in the areas of AI, Machine Learning Algorithms and Deep Learning Algorithms (with domain knowledge of Python).	40 Years	₹42,000/- +HRA as per rules
2913	HCP- 0042 (WP- 5.2)	Advanced Electronic Systems Area	Project Associate- I 01	B.E./ B.Tech/ M.Sc (EEE/ Electronics and Instrumentations/ Electronics/ ECE/ CSE) or equivalent. Desirable: Working experience and domain knowledge of AI, Machine Learning Algorithms, Deep Learning Algorithms and Python.	35 Years	₹31,000/- +HRA as per rules OR ₹25,000/- +HRA as per rules

2914	HCP- 0042 (WP- 6.1)	Advanced Electronic Systems Area	Project Associate- I 01	B.E./ B.Tech/ M.Sc (EEE/ Electronics and Instrumentations/ Electronics/ ECE/ CSE) or equivalent. Desirable: Working experience and domain knowledge of AI, Machine Learning Algorithms, Deep Learning Algorithms and Python.	35 Years	₹31,000/- +HRA as per rules OR ₹25,000/- +HRA as per rules
2915	HCP- 0042 (WP- 1.2)	Advanced Electronic Systems Area	Project Associate- I 01	B.E./ B.Tech/ M.Sc (Electronics/ Instrumentation/ Instrumentation and Control System/ Power Electronics/ Electrical Engg./ EEE/ Mechanical Engg./ Embedded System/ Robotics/ Mechatronics) or equivalent. Desirable: GATE/NET qualified candidates. Experience in Control Systems, Power Electronics, Embedded System, Programming in C/MATLAB, python, Data Science and Machine Learning etc.	35 Years	₹31,000/- +HRA as per rules OR ₹25,000/- +HRA as per rules
2916	MLP- 2025	Dairy and food Instru. Division	Project Associate- I 01 (For CSIR-CEERI, Jaipur Centre)	B.E./ B.Tech/ M.Sc (ECE/ Electrical & Electronics/ Electronics & Instrumentation/ Electronics/ CS/ IT/ Power Electronics) or equivalent.	35 Years	₹31,000/- +HRA as per rules OR ₹25,000/- +HRA as per rules
2917	HCP- 0034 (WP- 07)	Semiconductor Process Technology Group	Project Associate- I 01	B.E./ B.Tech/ M.Sc (Physics/ Applied Physics/ Optoelectronics/ Laser Physics/ Nano Technology/ E&C/ Electronics/ Radio Physics & Electronics) or equivalent. OR B.E./ B.Tech/ M.Sc (Physics/ Applied Physics/ Optoelectronics/ Laser Physics/ Nano Technology/ E&C/ Electronics/ Radio Physics & Electronics) or equivalent with valid NET/GATE Score.	35 Years	₹31,000/- +HRA as per rules OR ₹25,000/- +HRA as per rules
2918	GAP- 3417	Integrated Circuits and Systems Group	Junior Research Fellow 02	B.E./ B.Tech./ M.Sc (CSE/ IT/ ECE/ Robotics/ EE/ EEE/ Mechanical/ Instrumentation) or equivalent. Desirable: Working experience and domain knowledge of Image Processing, AI, Machine learning, Deep learning, Embedded System, Data fusion with working knowledge of C++ / Python.	35 Years	₹31,000/- +HRA as per rules OR ₹25,000/- +HRA as per rules
2919	HCP- 0042 (WP- 1.3)	Advanced Electronics Systems Area	Project Associate- II 01	B.E./ B.Tech/ M.Sc (EEE/ ECE/ E&I/ CS) or equivalent having Two Years' of experience in R&D in Industrial and Academic Institutions or Science and Technology, Organisations and Scientific Activities and Services. Desirable: M.E./ M.Tech/ MS (Microelectronics, VLSI, Embedded Systems, Electronics,	35 Years	*₹35,000/- +HRA as per rules OR *₹28,000/- +HRA as per rules

				Instrumentation) or equivalent having experience in AI algorithm development and implementation, understanding on circuits.		
2920	GAP- 3331	High Frequency Devices and Systems	Senior Project Fellow 01	M.E./ M.Tech (ECE/ Microwave/ RF and Microwave/ Microwave and RADAR) or equivalent. OR B.E./ B.Tech/ M.Sc (ECE/ Telecommunication/ Electronics & Tele-communications/ Electronics) or equivalent having two years' experience in relevant field. Desirable: Experience in using 3D electromagnetic simulation tools (I.e. CST design studio, Ansys HFSS, MAGIC etc.) and Matlab. Scholars who are selected through National Eligibility Tests-like CSIR-NET and GATE.	35 Years	₹35,000/- +HRA as per rules

- # (i) ₹31,000/- + HRA to Scholars who are selected through-
(a) National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or
(b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions.
(ii) ₹25,000/- + HRA for others who do not fall under (i) above.
- * (i) ₹35,000/- + HRA to Scholars who are selected through-
(a) National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or
(b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions.
(ii) ₹28,000/- + HRA for others who do not fall under (i) above.

Note: If any candidate wishes to apply for two or more projects or different positions in a project, then such candidates have to apply separately.

Interested candidates fulfilling the above qualifications must apply through online portal available on CEERI website. A print out of the Application Form must be taken by the candidate after successful submission of the form. The shortlisted candidates will be informed through E-mail/Institute website along with the date and time for interview. Candidates are requested to frequently browse the institute website. The eligible candidates must appear for interviews through "Microsoft Teams" (MS Teams). Candidates will be allowed as a Guest in MS Team, for which the link will be send to your registered email. The selected candidates are expected to join immediately upon selection and bring with them all ORIGINAL TESTIMONIALS including marksheets and certificates and also a passport size photograph at the time of joining, failing which their engagement can be withdrawn.

The last date & time for applying applications is 28.10.2022 till 05:00 P.M. Application received after last date will not be entertained.

For recruitment queries: Sh. Rajan Tirkey Section Officer CSIR-CEERI, Pilani Email: recruitment@ceeri.res.in Phone: 01596 – 252275	For technical queries: Sh. Sanjeev Kumar Network Management Cell CSIR-CEERI, Pilani Email: arvindkumar@ceeri.res.in Phone: 01596 – 252282
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Other Conditions:

- Candidates who have already served CSIR-CEERI or any other lab/ institute of CSIR as Project Staff such as Project Assistant/ Project Fellow/ JRF (in contract R&D Projects)/ SRF (in contract R&D Projects)/ Research Associate etc. for a total period of 5 years or more are not eligible for these engagements. The candidates who have served for a period less than 5 years will have tenure upto remaining period till completion of five years.

2. Tenure of engagement: Six months initially or co-terminus with the present project or till such time the job to be performed by Project Staff in the project exists, whichever is earlier. The tenure may be extended in steps of six months for the duration of Project based on satisfactory performance. The total tenure as Research Associate- I, Project Assistant, Project Associate-I/II, JRF/SRF etc. shall not exceed 5 years in any case. The total tenure of five years shall be calculated as per periods spent on one Project and/ or different Projects taken together in CSIR-CEERI, Pilani and/ or any other Lab./ Instt. of CSIR as RA, Project Assistant, Project Associate Level I/II etc. or as any other designation of equal status.
3. The date for determining the upper age limit, essential educational qualification and/or experience shall be the last date of submission of online application.
4. The engagement of Project Assistant, Project Associate- I/II, SPA, SRF, JRF etc. will be made on behalf of the Sponsor of the Projects. These are not CSIR-CEERI/CSIR posts and will not confer any right on the incumbent to claim, implicit or explicit, on any post in CSIR-CEERI/CSIR.
5. **Reservation:** As regards reservation, if all things are equal, SC/ST/OBC/PH/EWS candidates may be given preference over General candidates so as to ensure their representation.
6. Relaxation in age limit for SC/ST/OBC/PH and women shall be 5 years.
7. **Candidates shall upload scanned copies of following documents on Project Staff Recruitment Portal of CSIR-CEERI:-**
 - a. Candidates shall upload scanned copies of following academic certificates while applying for project staff positions:-
 - i. SSLC/10th
 - ii. HSC/Senior Secondary/Intermediate/12th
 - iii. Graduation Degree (B.A./B.Com./BBA/B.Sc. /B.E. /B.Tech. etc.)
 - iv. Post Graduation (M.Sc./M.E./M.Tech./MS etc.)

Educational Qualification Certificate as mentioned in the advertisement essential qualification / Semester Mark sheet or Final Mark sheet is compulsory. Along with this, Degree Award Certificate or Provisional Award Certificate. Percentage or CGPA/DGPA should be clearly displayed on any one of the certificate. If certificate doesn't have proper percentage / CGPA /DGPA/Grade then the application may not be considered. The candidate must ensure to provide proper certificates to ensure their eligibility for the positions they are applying for.
 - b. Date of Birth Proof (Any valid certificate having DoB such as SSLC, HSC, DoB Certificate, etc)
 - c. Category certificate (in case of SC/ST/OBC/PWD/EWS candidates)
 - d. NET/GATE/Any other equivalent exam qualification certificate (If applicable)
 - e. Experience certificate (Mandatory for the posts where experience is essential qualification)
 - f. Other certificates (If applicable)
8. If it is found at any stage of the recruitment process or thereafter that the candidates do not fulfil the eligibility criteria, their candidature shall be cancelled without assigning any reason. Further, the decision of CSIR-CEERI in regard to the selection process will be final and binding to all concerned. No correspondence in this regard will be entertained by the Institute.
9. Received applications will be screened by duly constituted Standing Screening Committee. The Screening Committee may fix different set of criteria to short-list the candidates as per the project requirement. The Screening Committee will screen all applications received for the specified project and recommend eligible candidates will be called for online interview. In case of false information received through online application, the competent authority will cancel the candidature of the applicant and debar for attending the interview in future.

10. Eligible applicants may be called for interview through online and Interview letter will be sent him/her through e-mail intimating the date & time of Interview, link/reference of online mode, terms & conditions, if any. The candidate will be allowed to join only after verification of original certificates and other information found correct.
11. Based on the performance of the candidates and availability of suitable candidates, Selection Committee may also recommend Panels for future engagement in different projects as and when need arises. The engagement process will be complete after the joining of the candidates.
12. CSIR-CEERI reserves the right to cancel or withdraw the Offer of engagement in case of any discrepancy found, in the candidature of any empanelled candidate at any stage.
13. The number of positions may be increased or decreased as per the requirements.
14. Other terms & conditions will be governed as per guidelines issued by the funding agency/ CSIR/ CEERI for the engagement of above Project Staff as amended from time to time.

Sd/-
CONTROLLER OF ADMINISTRATION